

IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Rajeev Bharadhwaj  
SERIAL NO.: Unknown (Parent Application No. 08/834,060)  
FILING DATE: November 13, 2001  
TITLE: System and Method for Multi-Level Memory Domain Protection  
EXAMINER: Unknown  
GROUP ART UNIT: Unknown  
ATTY.DKT.NO.: PA2314US

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Date: Nov. 13, 2001

David Lewis  
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Sir:

PRELIMINARY AMENDMENT

The Applicant respectfully requests that the Examiner enter the following amendments prior to examination of the above-identified application.

IN THE CLAIMS

Add the following new claims

28. A computer-implemented method for multi-level memory domain protection, comprising:

establishing a user process context for a user code;

establishing a domain process context for a domain code; and

protecting the domain code, executing at a protection level, from the user code, executing at the protection level, by context switching between the user process context and the domain process context,

wherein the user context process has a non-executable reserve portion in which the domain code is located.

29. A system for multi-level memory domain protection comprising:

a user process context for a user code;

a domain process context for a domain code; and

a protection that protects the domain code, executing at a protection level, from the user code, executing at the protection level, by context switching between the user process context and the domain process context,

wherein the user context process has a non-executable reserve portion in which the domain code is located.

30. A computer-readable medium embodying computer-readable program code for causing a computer to perform multi-level memory domain protection by performing the process of:

establishing a user process context for a user code;

establishing a domain process context for a domain code; and

protecting the domain code, executing at a protection level, from the user code, executing at the protection level, by context switching between the user process context and the domain process context, wherein the user context process has a non-executable reserve portion in which the domain code is located.

31. A computer-implemented method for multi-level memory domain protection, comprising:

protecting a domain code, executing at a protection level, from a user code, executing at the protection level, by context switching to establish two levels of protection within said protection level.

32. A computer-implemented method for multi-level memory domain protection, comprising:

executing calling-code in a first process pair calling for execution of targeted code in a second process pair; and

inter-group context switching from the first process pair to the second process pair, wherein the inter-group context switching establishes two levels of protection within said protection level.

33. A system for multi-level memory domain protection comprising:

a protection that protects a domain code, executing at a protection level, from a user code, executing at the protection level, formed by context switching to establish two levels of protection within said protection level.

34. A computer-readable medium embodying computer-readable program code for causing a computer to perform multi-level memory domain protection by performing the process of:

protecting a domain code executing at a protection level, from a user code, executing at the protection level, by context switching to establish two levels of protection within said protection level.

35. A system for multi-level memory domain protection comprising:

a protection that protects a domain code, executing at a protection level, from a user code, executing at the protection level, formed by context switching to establish two levels of protection within said protection level.

### CONCLUSION

The Applicant submits that the added claims, 28-35, recite the subject matter that placed the claims of the parent application in condition for allowance when added to the independent claims therein. The subject matter of the added claims is absent from the prior art of record. Claims 1-27 are allowable for the reasons explained in Applicant's arguments in the parent application. Therefore this application is in condition for allowance, and the Examiner is respectfully requested to allow this application. The Examiner is invited to contact the undersigned should there be any questions regarding this case.

Respectfully submitted,  
Rajeev Bharadhwaj

Date: Nov. 13, 2001

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